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Attorney's Docket No.: 01997-242002 / Case 8270S

DEC 22 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Arvind Mithal et al. Art Unit :
Serial No. : 10/690,261 Examiner :
Filed : October 21, 2003
Title : COMPUTER ARCHITECTURE FOR SHARED MEMORY ACCESS

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

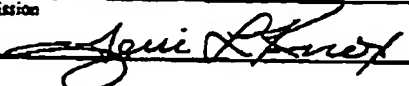
AMENDMENT IN REPLY TO ACTION OF SEPTEMBER 22, 2004

Please amend the above-identified application as follows:

01/03/2005 CCURTES 00000002 061050 10680261
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CERTIFICATE OF TRANSMISSION BY FACSIMILE

I hereby certify that this correspondence is being transmitted by facsimile to the Patent and Trademark Office on the date indicated below.

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REMARKS

Claims 2-26 were cancelled by preliminary amendment. Claim 1 stands rejected under double patenting over the claim 1 of US Pat. 6,636,950, as well as under 35 USC 102(b) over Liencres (US Pat. 5,765,196). Without conceding to the positions taken in the office action, the applicant has cancelled remaining claim 1 and added new claims 27 through 59, including new independent claims 27, 49, 52, and 59, to more clearly point out what is being claimed. The techniques described in Liencres do not anticipate or make obvious these claims. For example, with regard to claim 27, Liencres does not disclose or suggest "a first class of instructions whose semantics enable access a local memory for the processor without requiring coordination of data updates in a memory address space between the local memory and the shared memory system". As another example, with regard to claim 49, Liencres does not disclose or suggest "the instruction set including: instructions enabling uncoordinated memory access to the local memory system of a processor; and instructions enabling coordinated memory access by multiple processors."

Please apply \$175.00 for excess claim fees and any other charges or credits to deposit account 06-1050 referencing docket number 01997-242002.

Respectfully submitted,

Date:

Dec. 22, 2004

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